



# MRD-1000 SETTINGS

**3G/HD/SD SDI 1 to 8 Reclocking/Non-Reclocking Distribution Amplifier**

## INPUT AND OUTPUTS

### Rear:

BNC 1 = SDI INPUT	BNC 2 = SDI OUTPUT 1
BNC 3 = SDI OUTPUT 2	BNC 4 = SDI OUTPUT 3
BNC 5 = SDI OUTPUT 4	BNC 6 = SDI OUTPUT 5
BNC 6 = SDI OUTPUT 6	BNC 8 = SDI OUTPUT 7
BNC 7 = SDI OUTPUT 8	BNC 10 = Unused

### Front:

Front connector is SDI OUTPUT 9

BNC	Description
1	SDI INPUT
2	SDI OUTPUT 1
3	SDI OUTPUT 2
4	SDI OUTPUT 3
5	SDI OUTPUT 4
6	SDI OUTPUT 5
7	SDI OUTPUT 6
8	SDI OUTPUT 7
9	SDI OUTPUT 8
10	Reserved
FRONT	SDI OUTPUT

## DIP SWITCHES

Switch	Description	OFF	ON
1	Dip Switch Override	Disabled	Enabled
2	Reserved	X	X
3	Reserved	X	X
4	Reserved	X	X
5	Reserved	X	X
6	Reserved	X	X
7	Reserved	X	X
8	SDI Outputs	Un-Reclocked	Reclocked